

The Insecurity of Masked Comparisons: SCAs on ML-KEM's FO-Transform

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Post-Quantum Cryptography

We are in the process of migrating to Post-Quantum Cryptography.

NIST started a standardization process in 2016.

- Fourth round ongoing.
- Four candidates already selected.
- Kyber selected as KEM (Kyber \mapsto ML-KEM).

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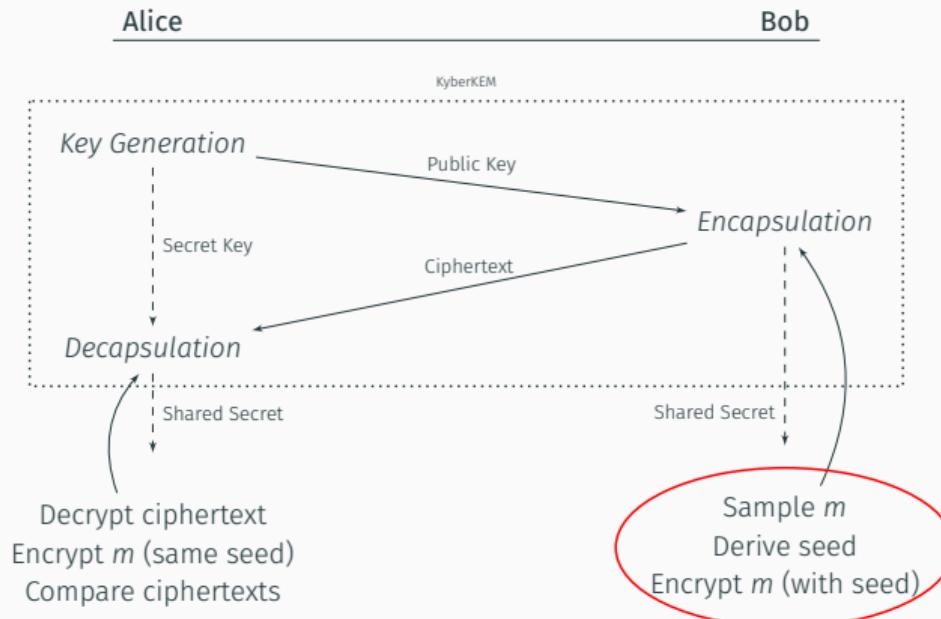
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We have to assume that usage on embedded devices will soon become widespread.

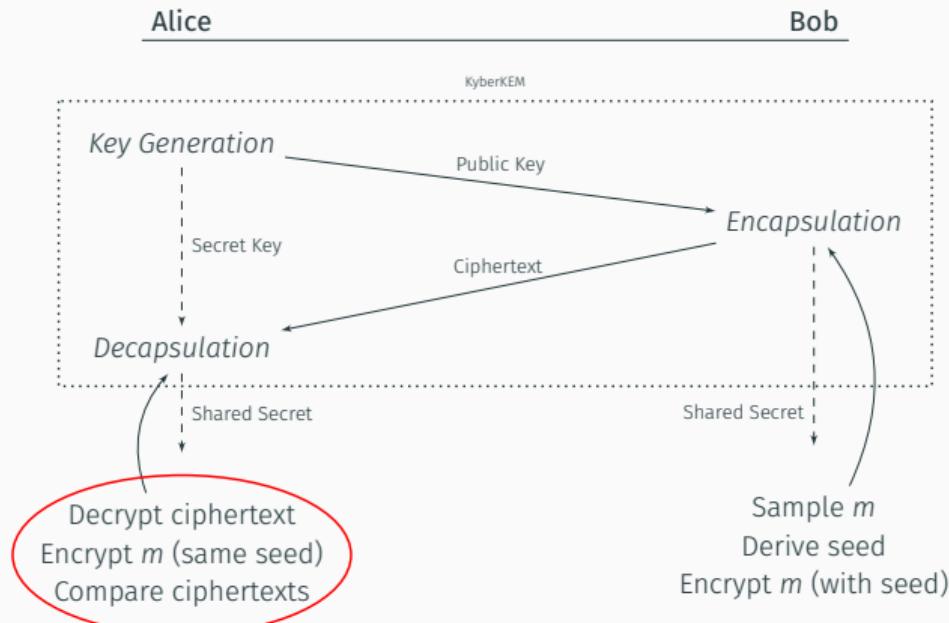
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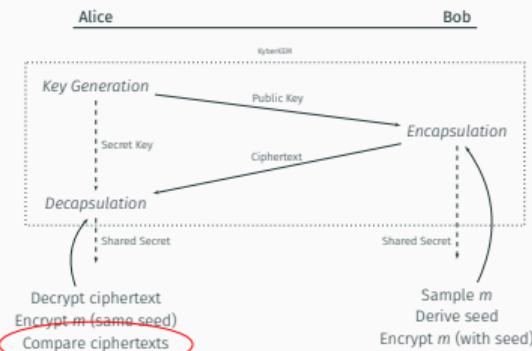
Masked Comparisons – A Highly Sensitive Operation

In ML-KEM:

- We compare a re-computed (ct') and a submitted ciphertext (ct).
- If outcome is leaked, chosen-ciphertext attacks are possible (see, e.g., [BDH+21; DHP+22; RRD+23]).

Attacker can force two cases (see, e.g., [BDH+21]):

1. ct and ct' differ in one coefficient.
2. ct and ct' differ in about half the bits.



On embedded devices, we have to protect against power side channels.

Masked Comparisons – Most Recent Proposal

Most recent protected method [DBV23] works by

- $\Delta ct = ct - ct'$ in Boolean masking.
- Multiply shares of coefficients with random value over finite field.
- Check if shares sums zero.

Attacker targets Boolean shared Δct :

1	0	1	0	1	1	1	0	1	1	0	...
\oplus	0	1	1	0	0	1	0	0	0	1	...
\oplus	1	1	0	1	0	0	1	1	1	0	...
\oplus	0	1	0	1	1	1	0	0	0	1	...
<hr/>											
0	1	0	0	0	1	1	0	0	0	0	...

Unshared bits of first coefficient of Δct

Formally verified in the t -probing model.

Building a Leakage Model

Implementation needs to multiply Δ_{ct} -bits with random value

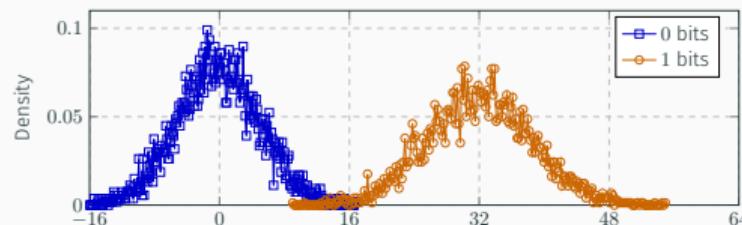
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Our model (simulation for $\sigma = 5$):



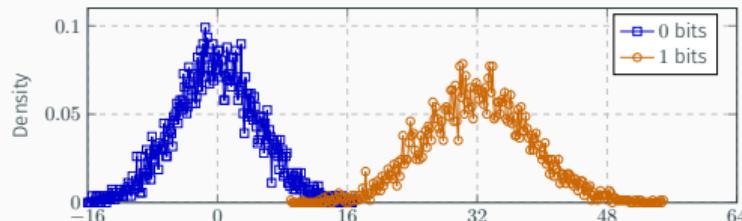
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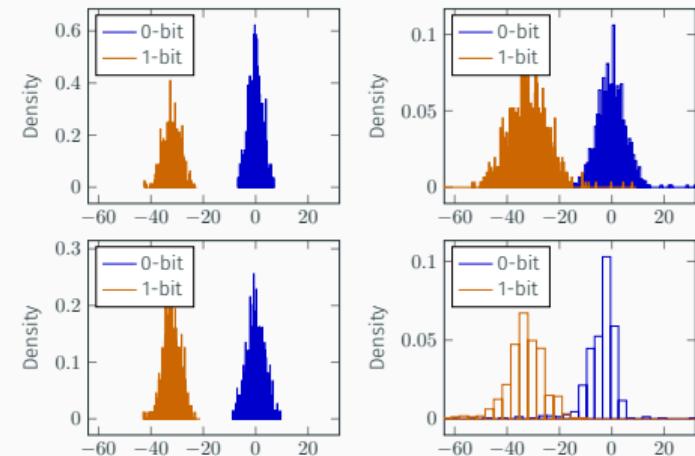
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Power consumption of processing a single shared bit.

Actual leakage confirms our model:



Designing Attacks

How to classify traces based on our leakage model?

General attack:

1. Submit n chosen-ciphertexts potentially causing decryption failures and record power traces.
2. Classify into decryption failures and decryption successes.
3. Derive inequalities, recover secret key with [HMS+23].

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Classifying traces based on model:

- Goal: Learn distributions for 0 and 1 bits for each bit of Δ_{ct} .
- Then: Classify bits based on measurement and distribution.
- Based on “reliably” classified bits: Decide if failure (at least one 1-bit) or success (only 0-bits).

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To classify trace: Classifying one 1-bit reliably suffices.

To recover secret key: 55% trace classification success rate suffices.

Designing Attacks

How to classify traces based on our leakage model?

Instead of a profiled attack:

Shared bits correspond to locations in power trace.

- Each ciphertext gives trace.
- Vertical: Over multiple traces, same relative location.
- Horizontal: Same trace, different locations.

Vertical Analysis: Learn joint distribution individually for each shared bit from all traces.

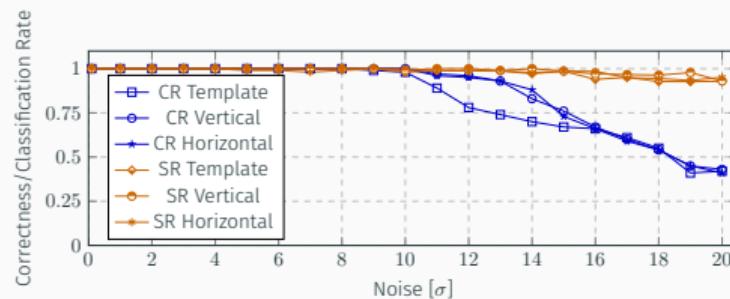
Horizontal Analysis: Learn (the same) joint distribution for all shared bits from one trace.

Then: Separate distributions into two normal distributions.

Results

We simulated the attacks for different noise levels.

Simulated results with 4 shares:

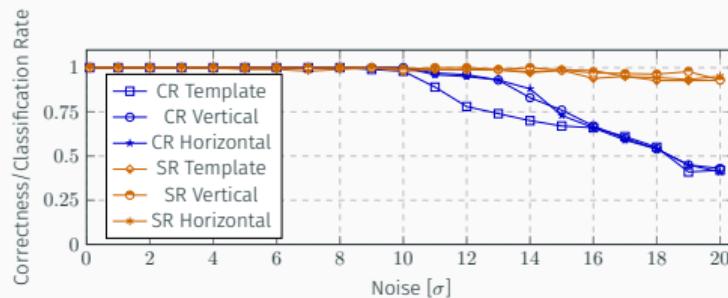


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Why do these attacks work so well?

- Information (1 bit!) is stored/processed in several hundred bits.
- Slight advantage over guessing suffices for attack.
- No instructions for used arithmetic amplifies leakage.

→ High noise requirements.

Summary and Conclusion

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To assess the security of a recent masked comparison proposal, we:

- Built a leakage model based on the noisy HW model.
- Derived several attacks working under high noise/masking orders.
- Replaced profiling by vertical/horizontal analysis.
- Verified model and attacks on several devices.

Conclusion

In particular for post-quantum schemes:

- Even if t -probing secure, noise/masking orders necessary to prevent the attack in practice may be unrealistically high.
- Commonly used methodology ignores factors that are highly relevant for post-quantum schemes.

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Thank you for your attention!

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[BDH+21] Shivam Bhasin, Jan-Pieter D'Anvers, Daniel Heinz, Thomas Pöppelmann, and Michiel Van Beirendonck. "Attacking and Defending Masked Polynomial Comparison for Lattice-Based Cryptography". In: *IACR Trans. Cryptogr. Hardw. Embed. Syst.* 2021.3 (2021), pp. 334–359. URL: <https://doi.org/10.46586/tches.v2021.i3.334-359>.

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